

coincidence (Yes), the arithmetic target data 402 is subjected to a process A in step S405, and thereafter the operation proceeds to step S406.

Page 21, second full paragraph, to Page 22, replace with the following:

Step S206 is a step for repeating the processes of steps S201 to S205 until the processing for 8 pieces of data as the arithmetic target data has been finished. When j is smaller than "8" (Yes) in step S206, "1" is added to j in step S207, and the process of step S201 and later steps are repeated. By these steps S206 and S207, the control processing step which comprises the pipeline processing having the first stage 310 to the third stage 312 and in which the control for executing the vector arithmetic processing is exerted by executing the pipeline processing, as well as the control is exerted to execute the arithmetic processing when the information output by the condition decision processing indicates that the condition is satisfied, and directly output data which are supplied to a predetermined source among the data which are supplied by the source data supply process as the arithmetic result of the